The Group is activated in the areas of analog circuit design, analog testing and fault diagnosis, analog signal processing and neural network implementations, integrated inductor design, CAD tool development.

It is equipped with the necessary laboratory infrastructure and the required suitable experimental setups.

It has international collaborations and funded research projects.

#### Dr. Alkiviadis A. Hatzopoulos

Professor Director of the Electronics Lab.

alkis@eng.auth.gr,

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Research associates: Dr. Dimitrios Papakostas dpapakos@el.teithe.gr Dr. Michael Dimopoulos mdimop@ieee.org Dr. Michail Kiziroglou m.kiziroglou@imperial.ac.uk Dr. Yiorgos Mpontzios bontzios@gmail.com

Ph.D. students: Nikos Tsatsoulis Vasilios Gerakis

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e-mail: alkis@eng.auth.gr Tel: +30 2310-996305 Fax: +30 2310-996447

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## Aristotle University of Thessaloniki, GREECE

## Department of Electrical and Computer Engineering



Electronics Lab

## Main scientific areas

The scientific areas in which the Group is activated are:

Design and fault diagnosis of analog and mixed-signal integrated circuits.

➤ 3D Integrated circuits

Telecommunication electronics

Development of CAD tools

Design and optimisation of integrated inductors.

>Thin-Film-Transistors

Substrate noise modelling

**Bioelectronic circuits.** 

Electronics of Measuring Systems.

>Analog signal processing circuits.

>Neural circuits and realisation in analog integrated circuits.

# Laboratory Equipment

The existing laboratory equipment is modern and all scientific areas can be sufficiently supported. Measurements up to 65 GHz can be made.





#### Collaborations

Telecommunications Lab, Dept. of Electrical & Computer Eng., Aristotle Univ. of Thessaloniki, Greece.

► Electronics Lab, Dept. of Physics, Aristotle Univ. of Thessaloniki, Greece.

► Electronics Lab, Dept. of Electrical & Computer Eng. , National Technical Univ. of Athens, Greece.

ESAT, Katholieke Universiteit Leuven, (KUL), Belgium.

**EPFL**, Lausanne, Switzerland

Electrotechnics Dept., Technical Univ. of Cluj-Napoca, Romania.

**ELIS**, University of Gent, Belgium.

**LAAS du CNRS, Toulouse, France.** 

> Department of Physics, University of Balearic Islands, Palma, Spain

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 ✓ European Union
✓ General Secretary of research and Technology (Greece).

✓ Greek Ministry of Education